

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Keiichiro KATA et al.

Title: PROCESS FOR MANUFACTURING
SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR WAFER

Patent No.: RE39,603

Reissue Date: 05/01/2007

Appl. No.: 10/645,782

Filing Date: 08/22/2003

Examiner: Mark V. Prenty

Art Unit: 2822

Confirmation 8077
Number:

PETITION TO CORRECT NAMES OF ASSIGNEES ON AN ISSUED PATENT
UNDER 37 C.F.R. § 1.183

Mail Stop Petitions
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants respectfully request that 37 C.F.R. §3.81(a) be waived to permit the correct names of the assignees to be provided after issuance for the above-identified patent.

Applicants hereby state that the failure to include the correct assignee names on Form PTOL-85B was inadvertent and enclose herewith a copy of the Notice of Recordation of Assignment Document and a Request for Certificate of Correction, including Form PTO/SB/44 indicating the corrections to be made.

A credit card payment in the amount of \$130.00 is enclosed for the amount stated in

37 C.F.R. § 1.17(h) to cover the fee for this petition.

Adjustment date: 06/02/2008 CKHLOK
02/11/2008 INTEFSW 00000324 10645782
01 FC:1464 -130.00 OP

06/02/2008 CKHLOK 00000013 RE39603
01 FC:1808 130.00 OP
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The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment form being unsigned, providing incorrect information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date February 8, 2008

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